

Amendments to the Abstract:

Applicants amend the Abstract as follows:

According to one embodiment of the invention, a [[A]] method for processing registers in an out-of-order processor is provided. The technique substantially simultaneously reads original values from a plurality of registers. A particular instruction is parsed. Results of a dependency-producing instruction are determined. A select number of registers to be modified is determined based on the instruction. The selected number of registers is modified. The values are substantially simultaneously written to the plurality of registers. A subset of the values in the select number of registers is modified with architecturally correct values including the results of the dependency-producing instruction. The original values and the architecturally correct values are simultaneously written to the plurality of registers.